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	APPLICANT Marc Tremblay, et al.	
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U.S. PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

/KP/	"Lock-Based Programs and Transactional Lock-Free Execution", by Ravi Rajwar et al., University of Wisconsin-Madison Technical Report #1440, April 2002.
/KP/	"Speculative Locks for Concurrent Execution of Critical Sections in Shared-Memory Multiprocessors", by Jose F Martinez et al., Workshop on Memory Performance Issues, International Symposium on Computer Architecture, June 2001.
/KP/	"Transactional Memory: Architectural Support for Lock-Free Data Structures", by Maurice Herlihy.
/KP/	"Speculative Locks for Concurrent Execution of Critical Sections in Shared-Memory Multiprocessors", by Jose F. Martinez et al., Technical Report UIUCDCS-R-2001-2202, February 2001.
/KP/	"Transactional Lock-Free Execution of Lock-Based Programs", by Ravi Rajwar, Proceedings of the Tenth International Conference on Architectural Support for Programming Languages and Operating Systems, Oct. 6-Oct. 9, 2002, San Jose, CA.

EXAMINER /Kaushikkumar Patel/ (04/25/2007)	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant.

Attorney Docket No. SUN-P9329-MEG